

# **LOGICAL TO PHYSICAL ADDRESS MAPPING OF CHIP SELECTS**

## **ABSTRACT OF THE DISCLOSURE**

In some embodiments, a system and method for mapping the logical chip selects to a physical chip select. A chip select remapping unit receives logical chip select associated with a dual in-line memory module. The chip select remapping unit converts the logical chip select vector through a redirection table that maps the logical memory ranks to available physical memory ranks.